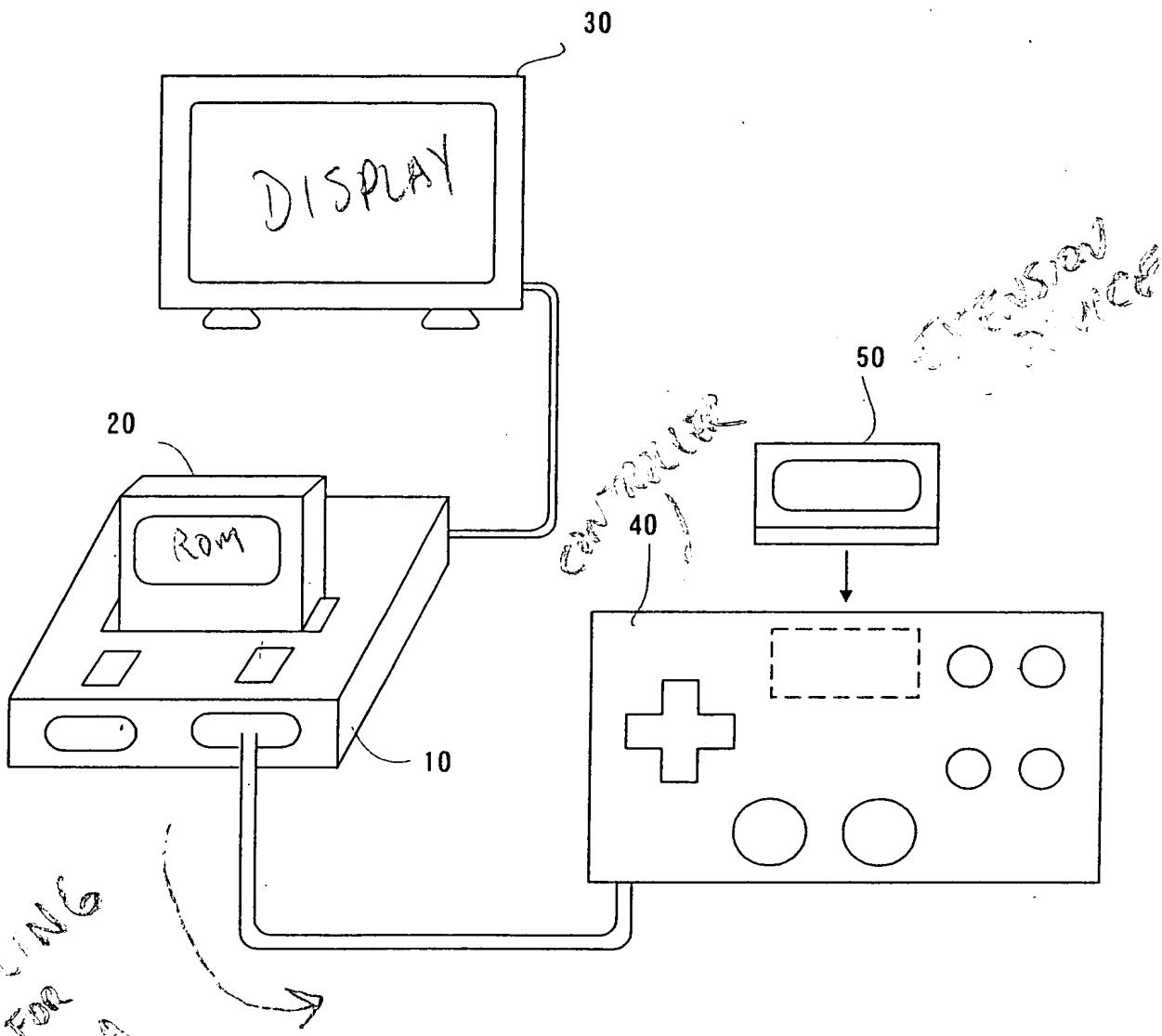


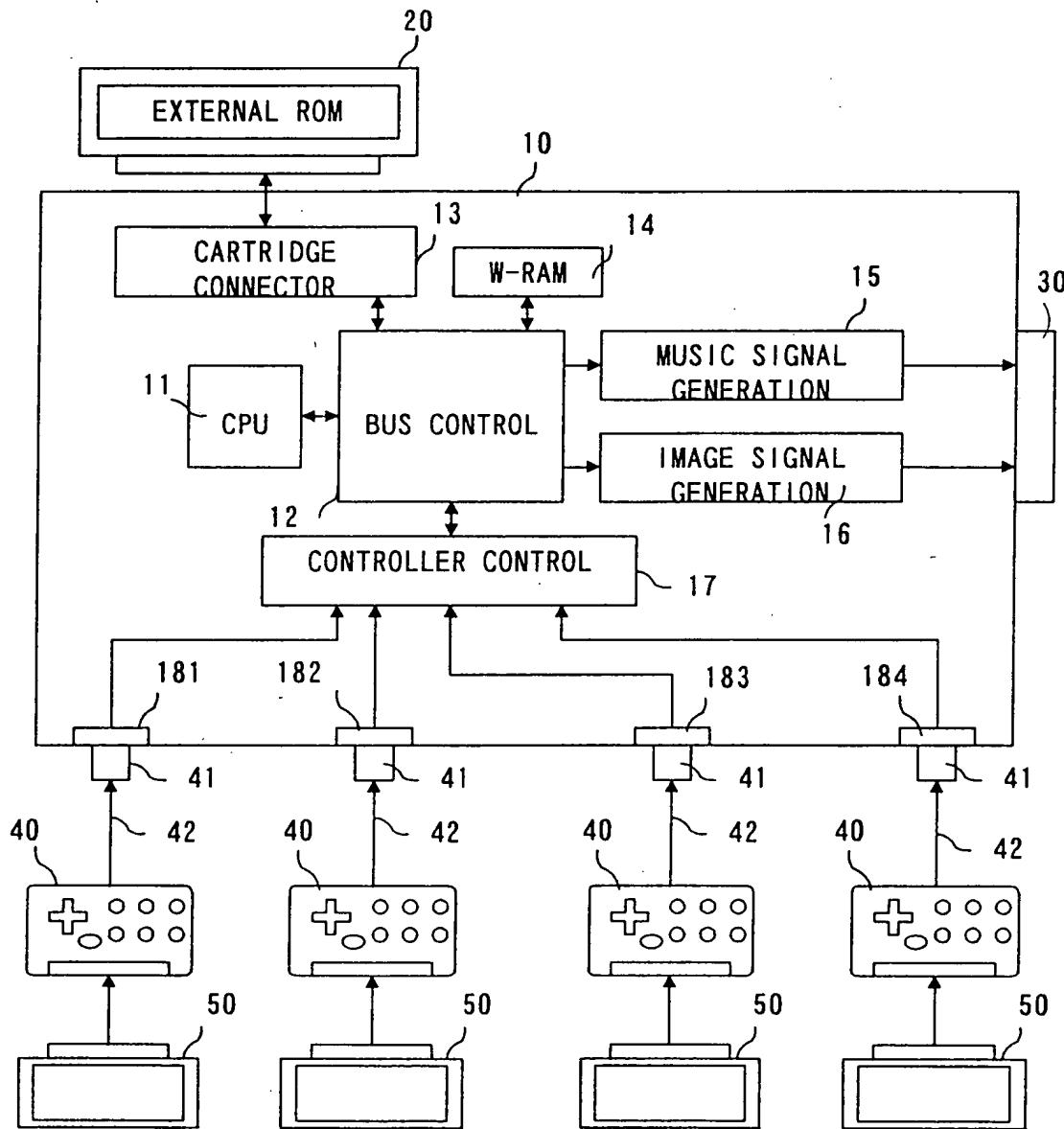
APPROVED	O.G. FIG.
BY	CLASS
DRAFTSMAN	SUBCLASS

FIG. 1



552070-0000-0000

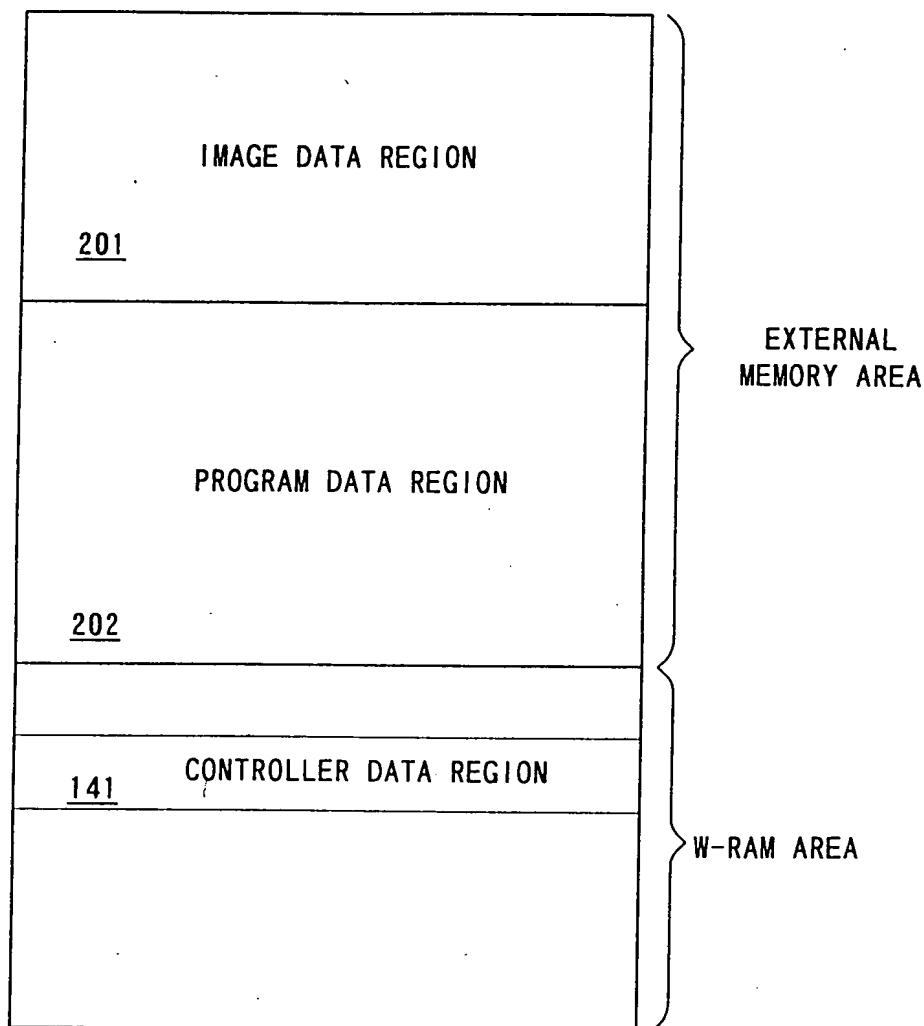
FIG. 2



APPROVED BY O.G. FIG.
CLASS SLRC 48
DRAFTSMAN

FIG. 3

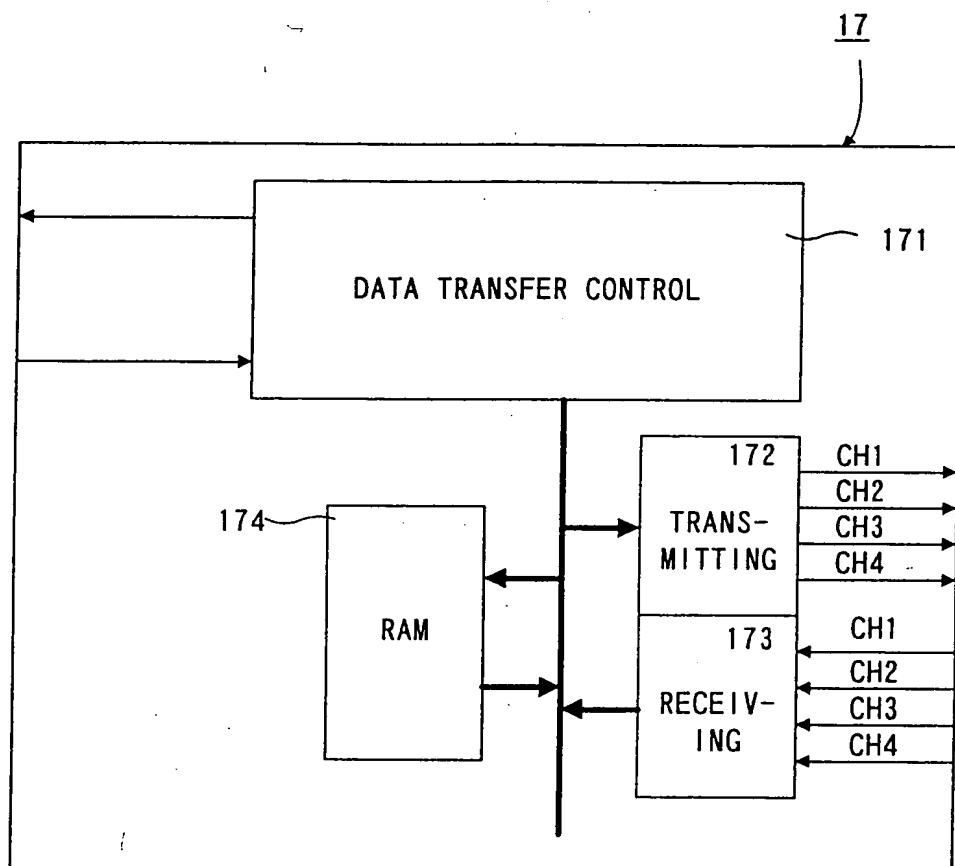
CPU MEMORY MAP



0000000000000000

APPROVED O.G. FIG.
BY CLASS SUBCLASS
DRAFTSMAN

FIG. 4



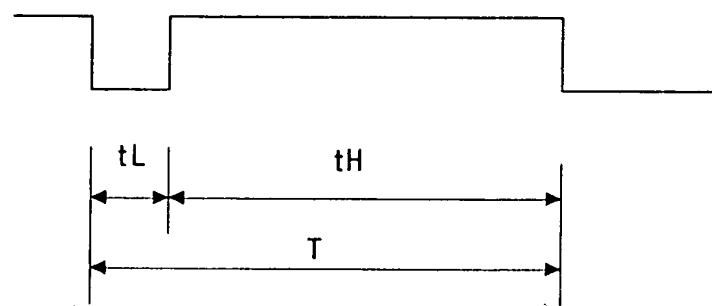
650070 0000000000000000

APPROVED
BY
DRAFTSMAN

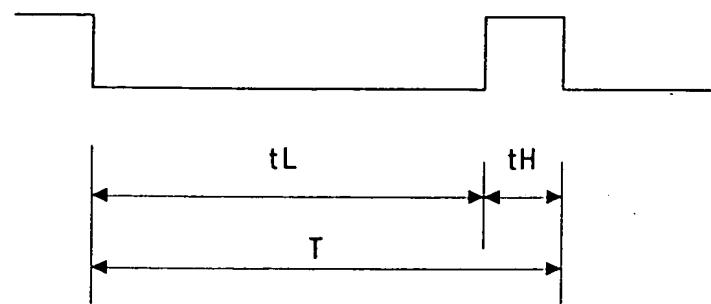
O.G. FIG.
CLASS SUBCLASS

FIG. 5

"1" SIGNAL



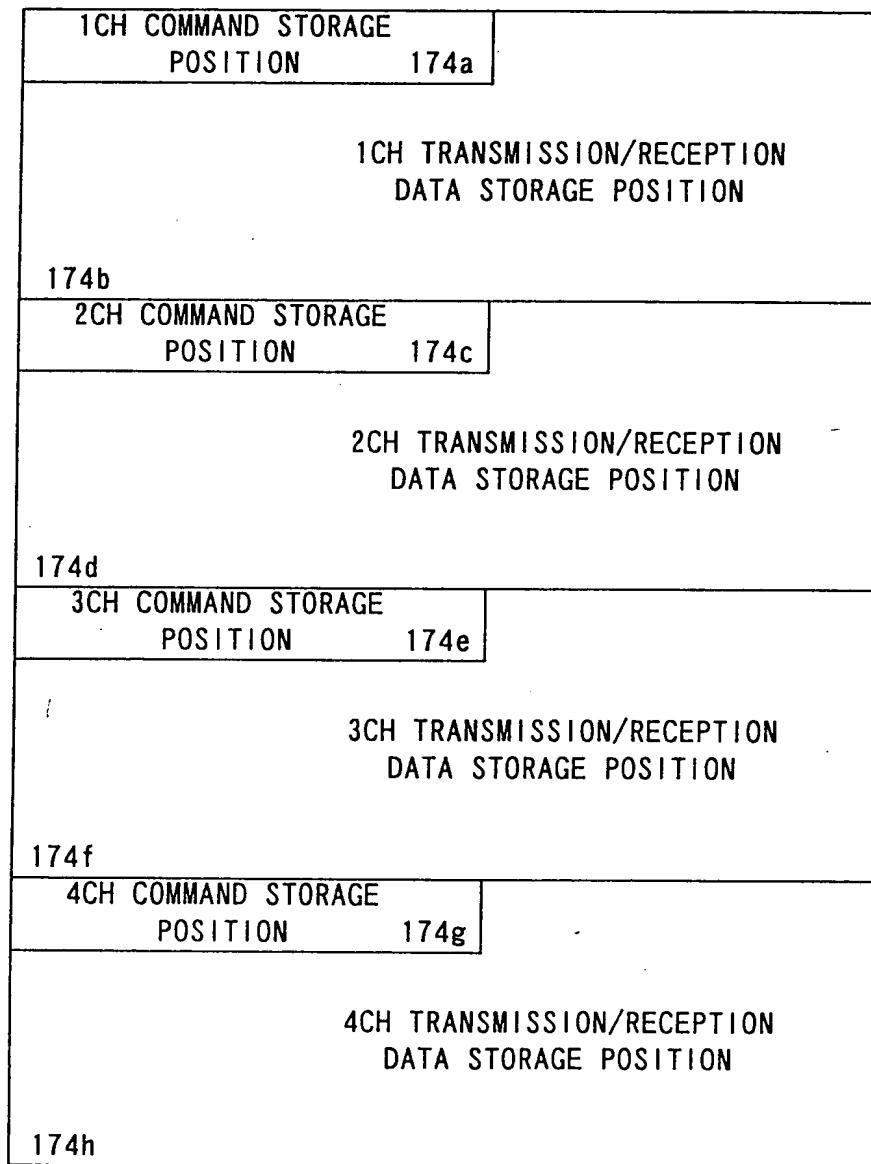
"0" SIGNAL



APPROVED O.G FIG
BY CLASS SUBCLASS
DRAFTSMAN

FIG. 6

RAM 174



1980 SEP 26 2 26 30

APPROVED O.G. FIG.
DRAFTSMAN

FIG. 7

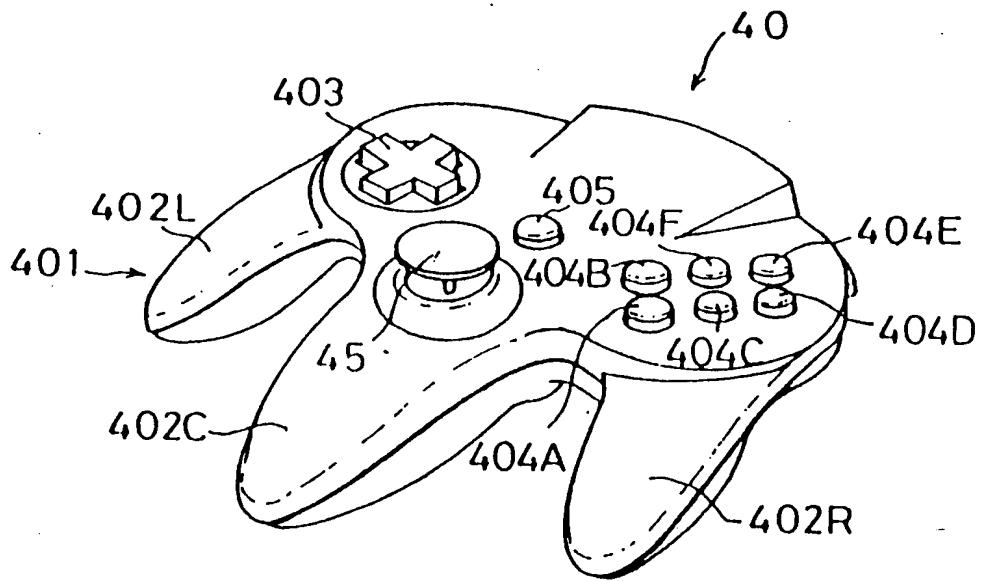


FIG. 8

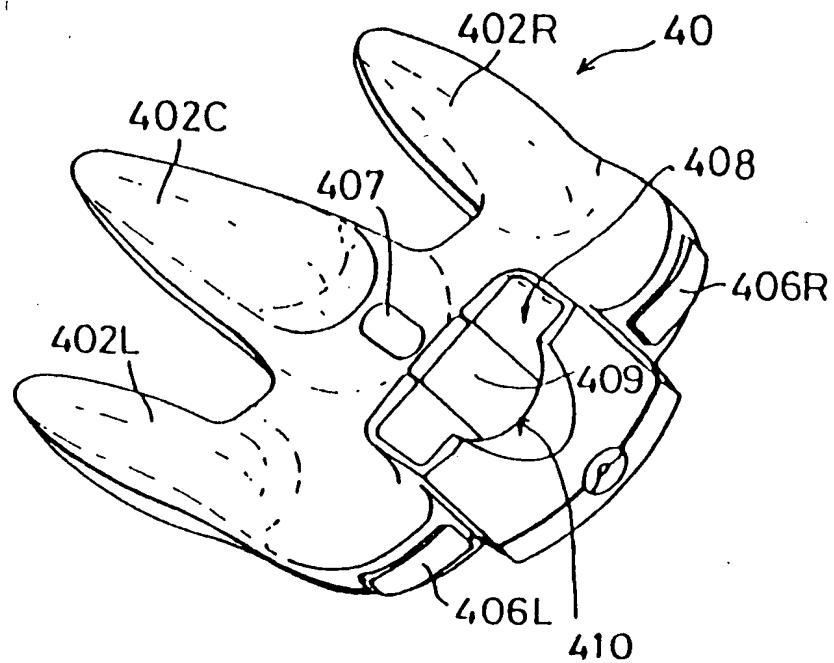


FIG. 9

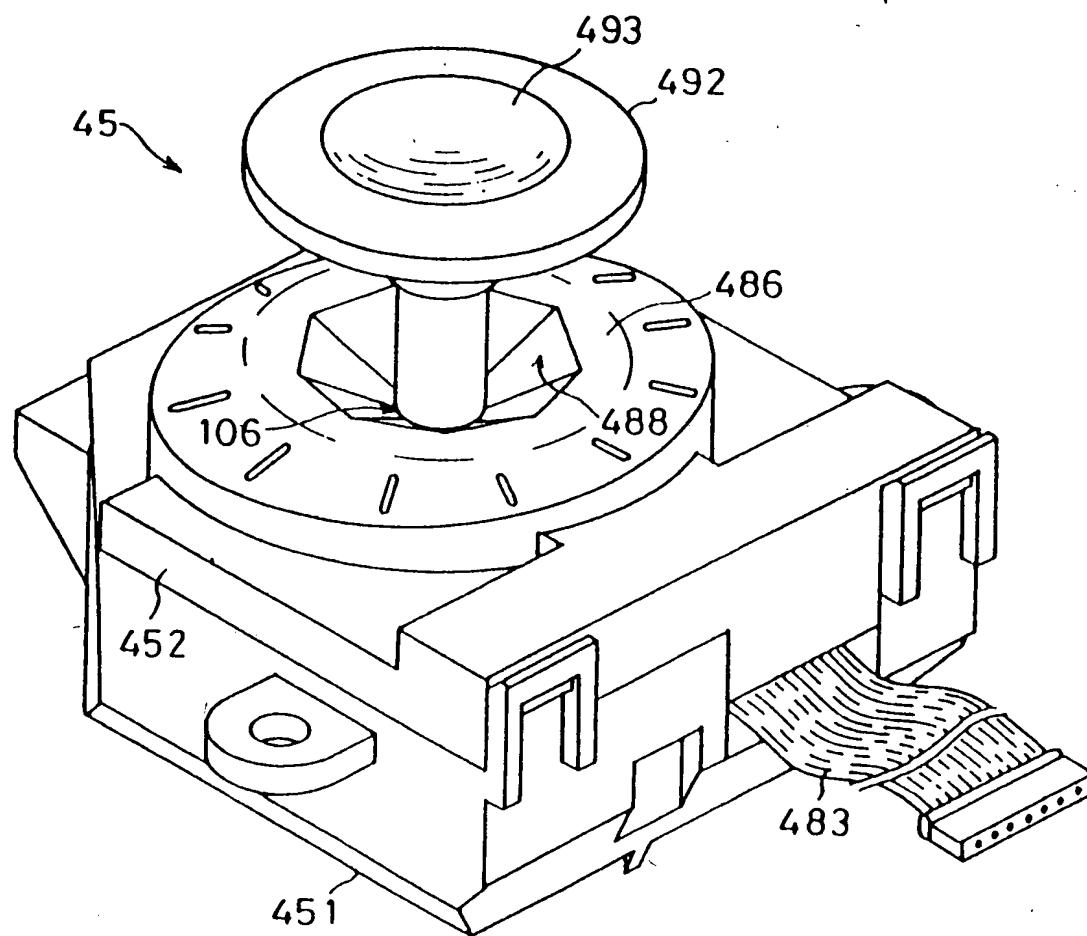
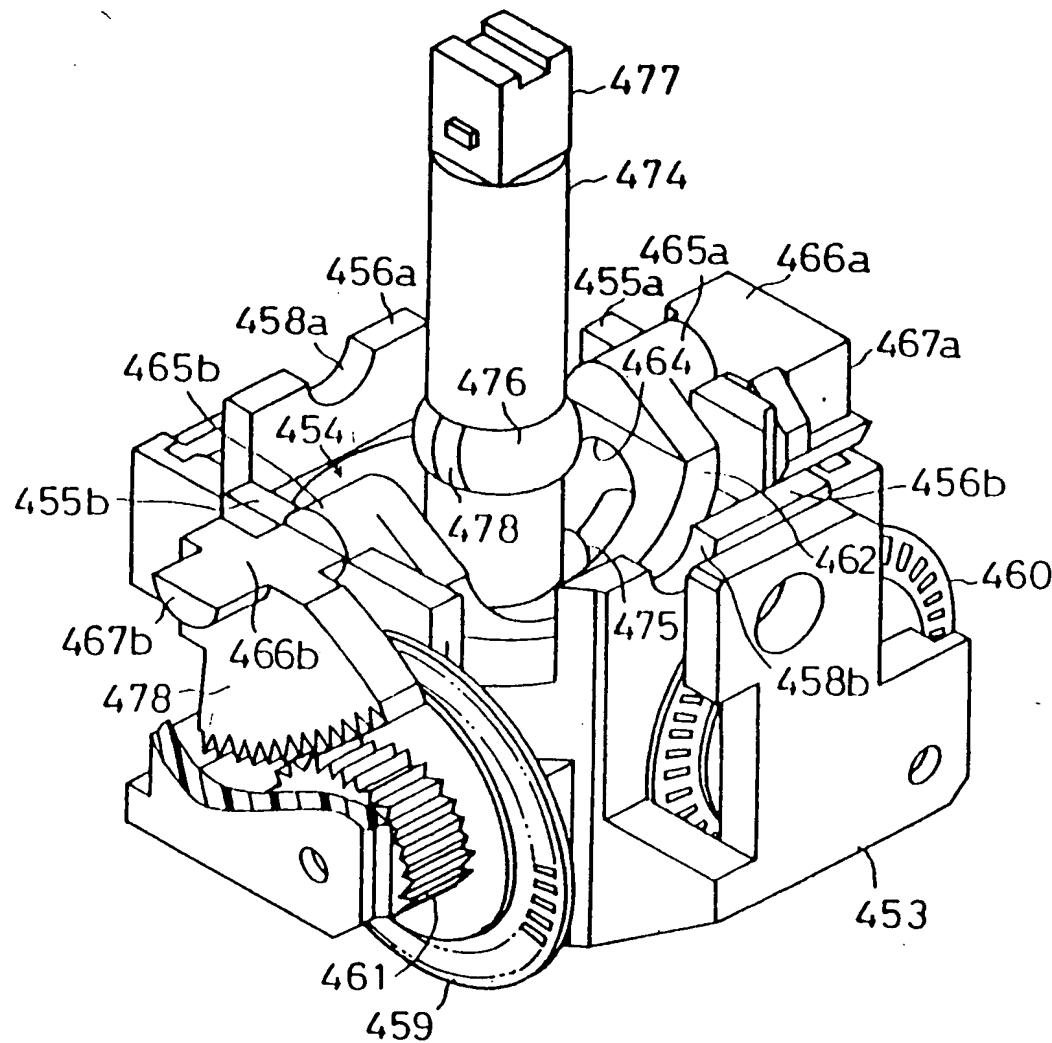
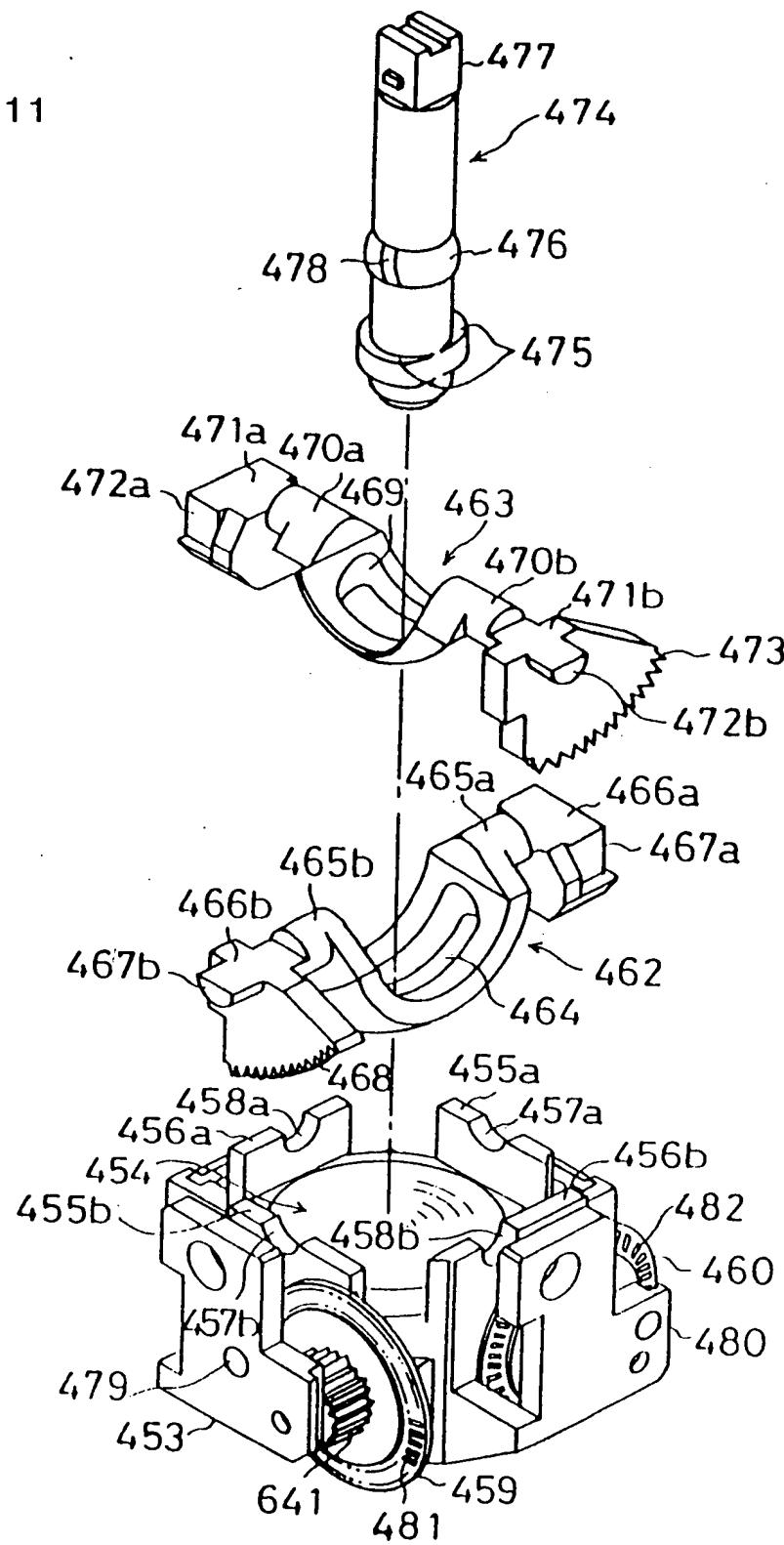


FIG. 10



APPROVED	O.G. FIG.
BY	CLASS SUBCLASS
DRAFTSMAN	

FIG. 11



APPROVED BY O.G. FIG
CLASS SUBCLAS
DRAFTSMAN

FIG. 12

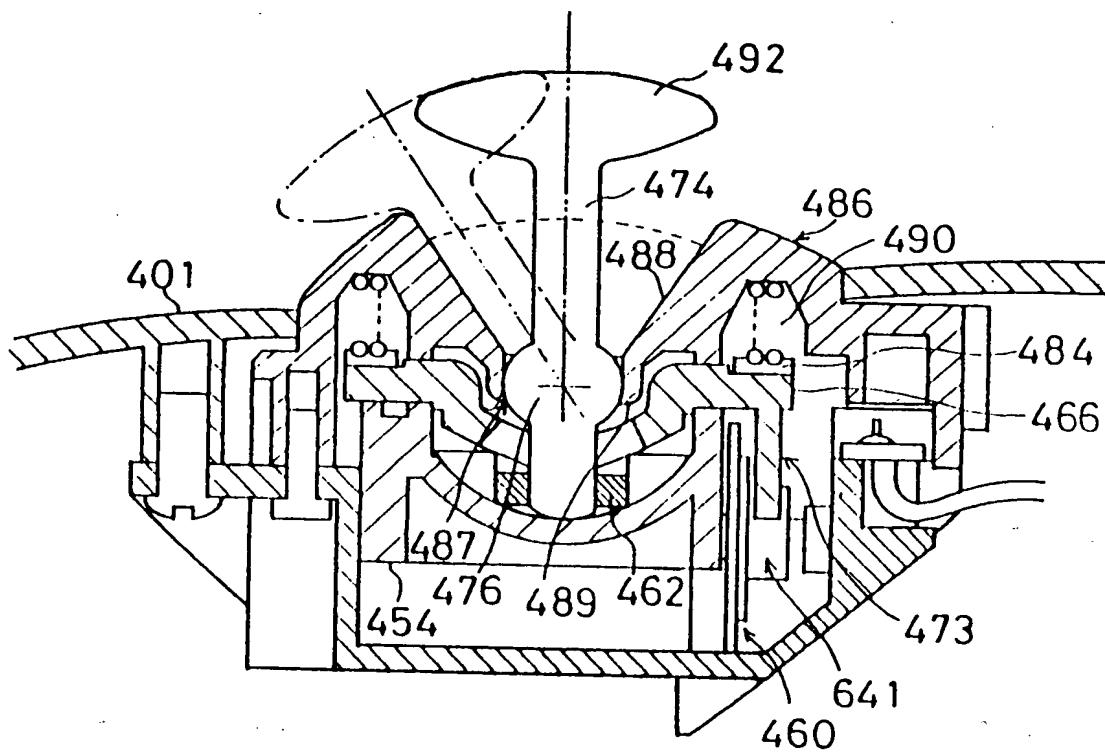
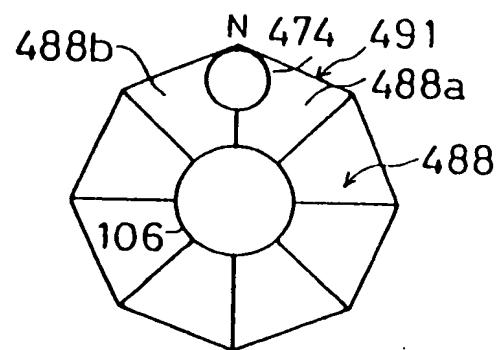
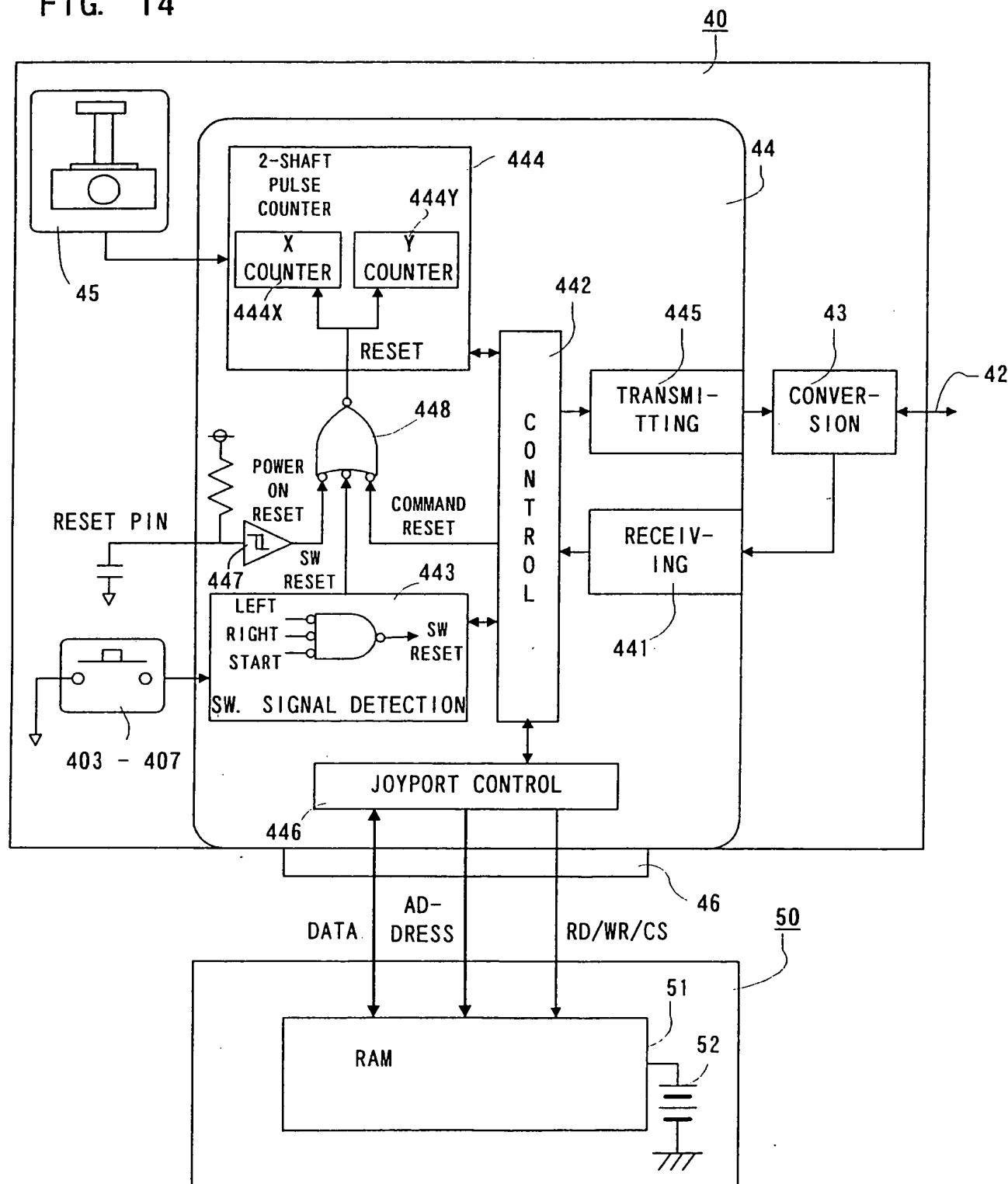


FIG. 13



APPROVED	O.G. FIG.
BY	CLASS
DRAFTSMAN	SUBCLASS

FIG. 14



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 15

APPROVED BY DRAFTSMAN	O.G. FIG.
	CLASS SUBCLASS

FIG. 16

COMMAND 0: TRANSMITTING TYPE OF CONTROLLER
RECEPTION: 1 BYTE TRANSMISSION: 3 BYTES

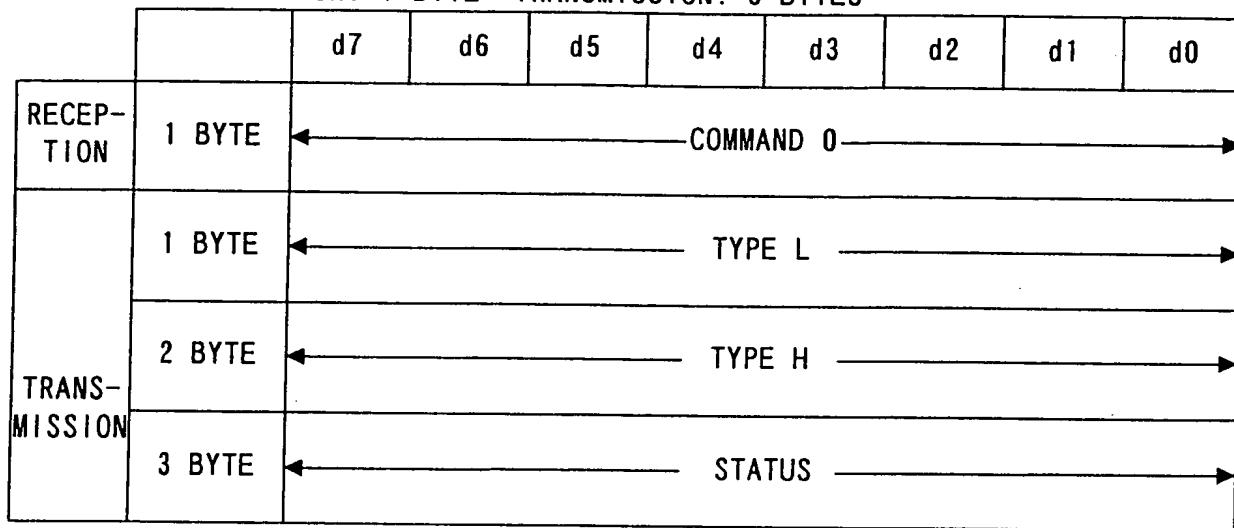
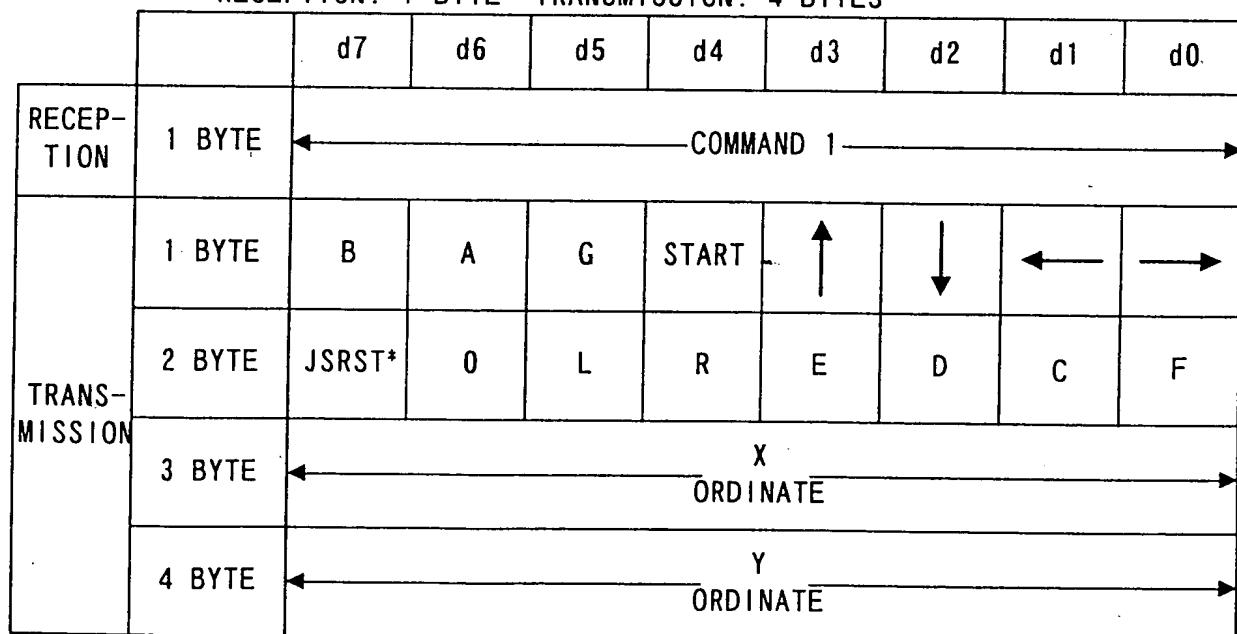


FIG. 17

COMMAND 1: ACCESSING STANDARD CONTROLLER
RECEPTION: 1 BYTE TRANSMISSION: 4 BYTES

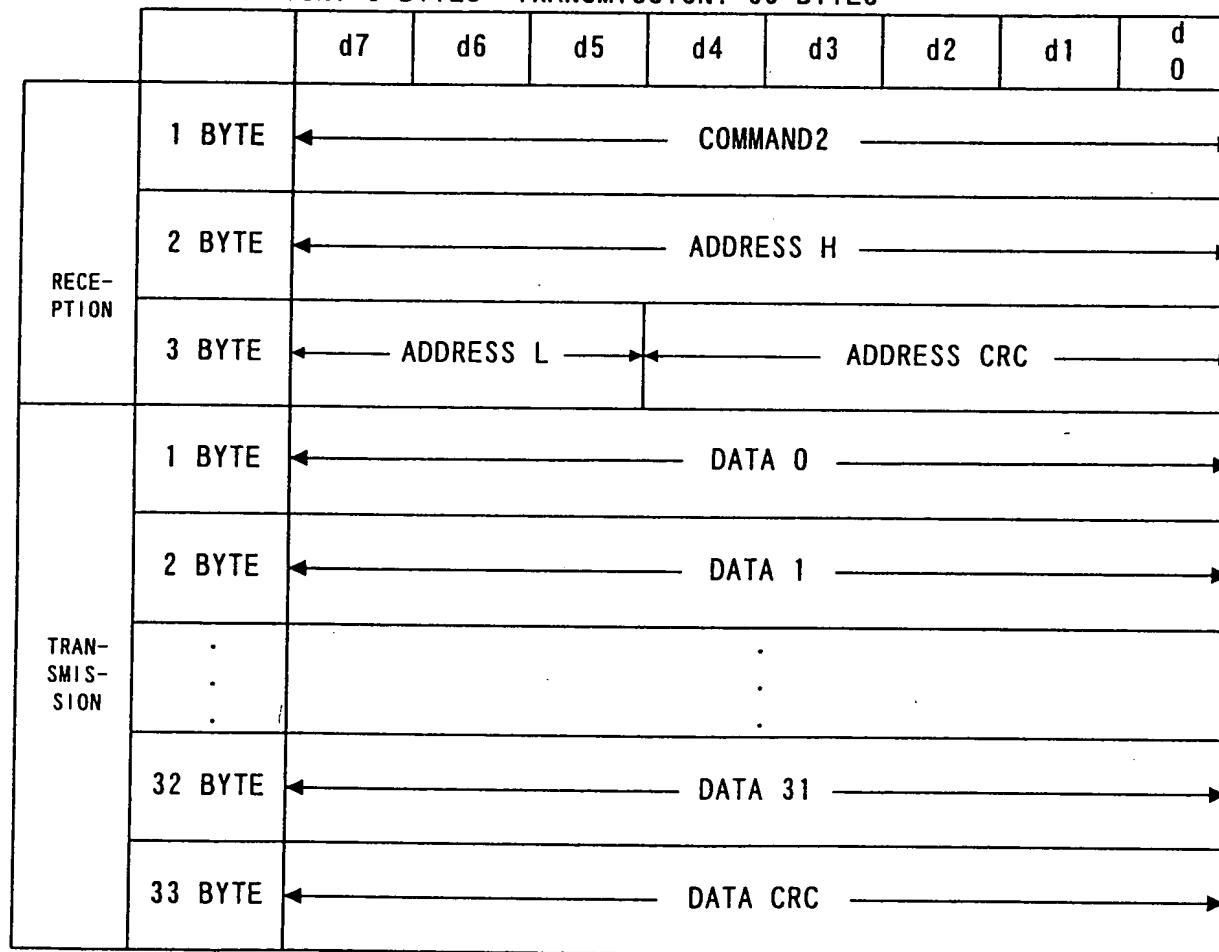


* HIGH LEVEL WHEN L, R AND START BUTTONS ARE
SIMULTANEOUSLY DEPRESSED

APPROVED BY DRAFTSMAN	O.G. FIG. CLASSE SUBCLASS
-----------------------------	------------------------------

FIG. 18

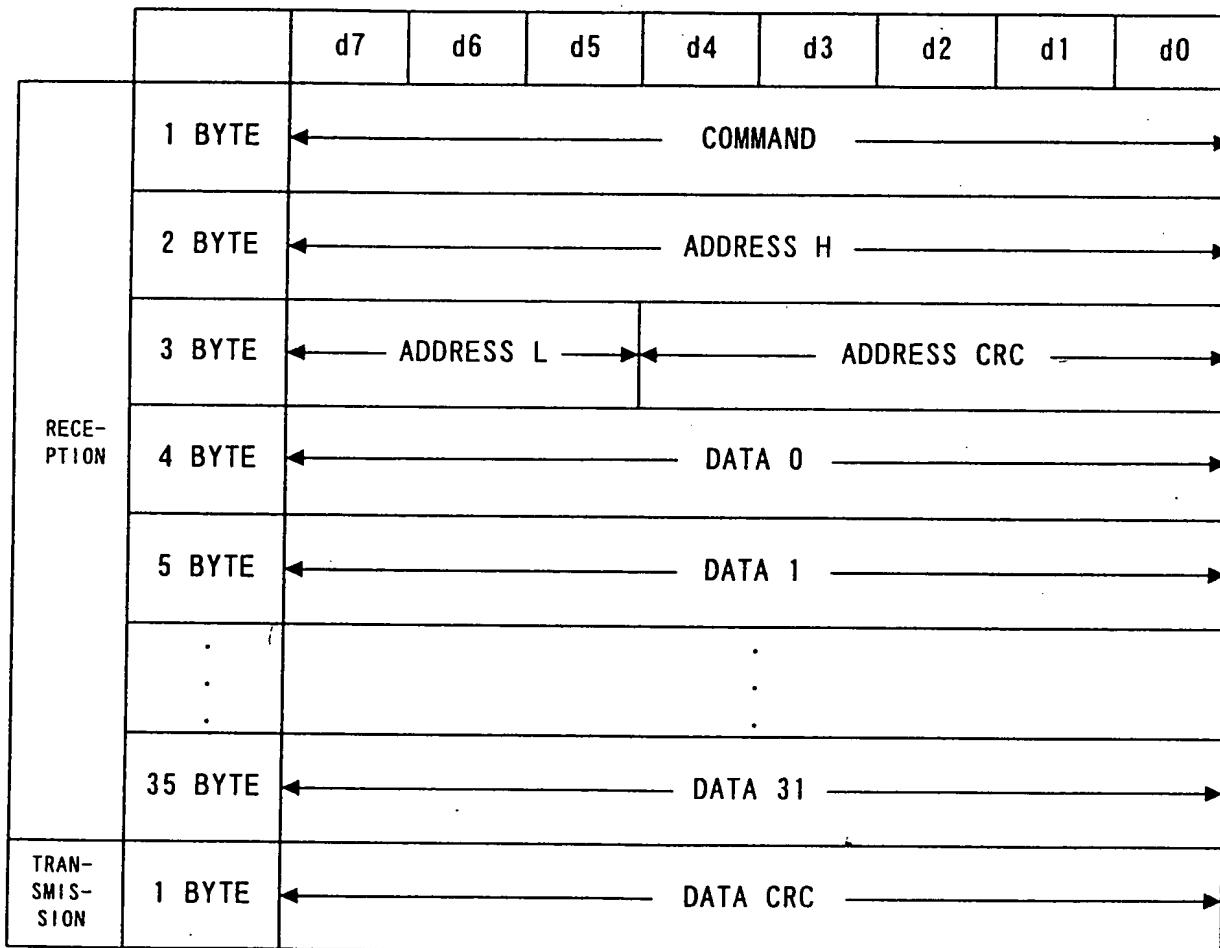
COMMAND 2: READ-OUT RAM
 RECEPTION: 3 BYTES TRANSMISSION: 33 BYTES



APPROVED BY	O.G. FIG.
DRAFTSMAN	CLASS SUBCLASS

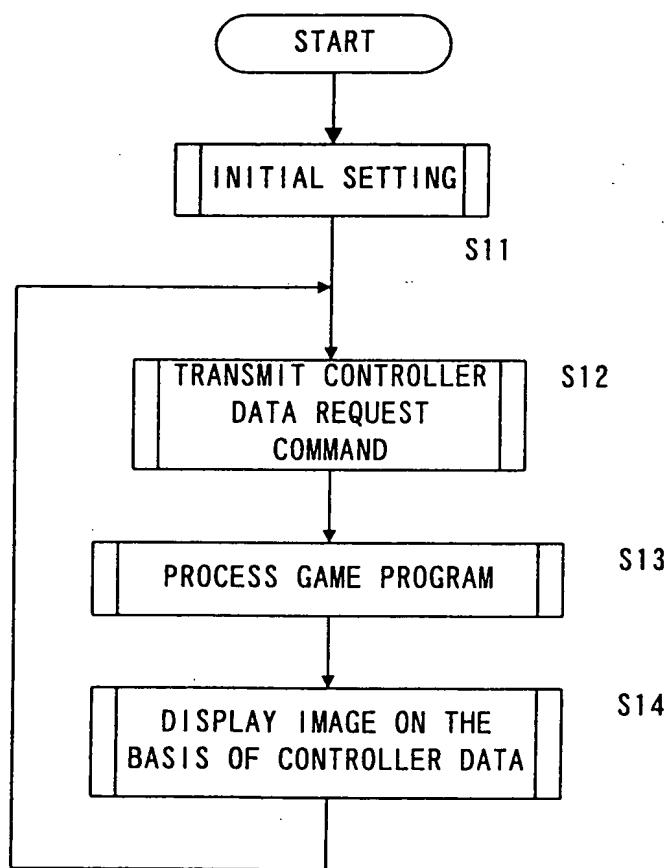
FIG. 19

COMMAND 3: WRITE TO RAM
 RECEPTION: 35 BYTES TRANSMISSION: 1 BYTES



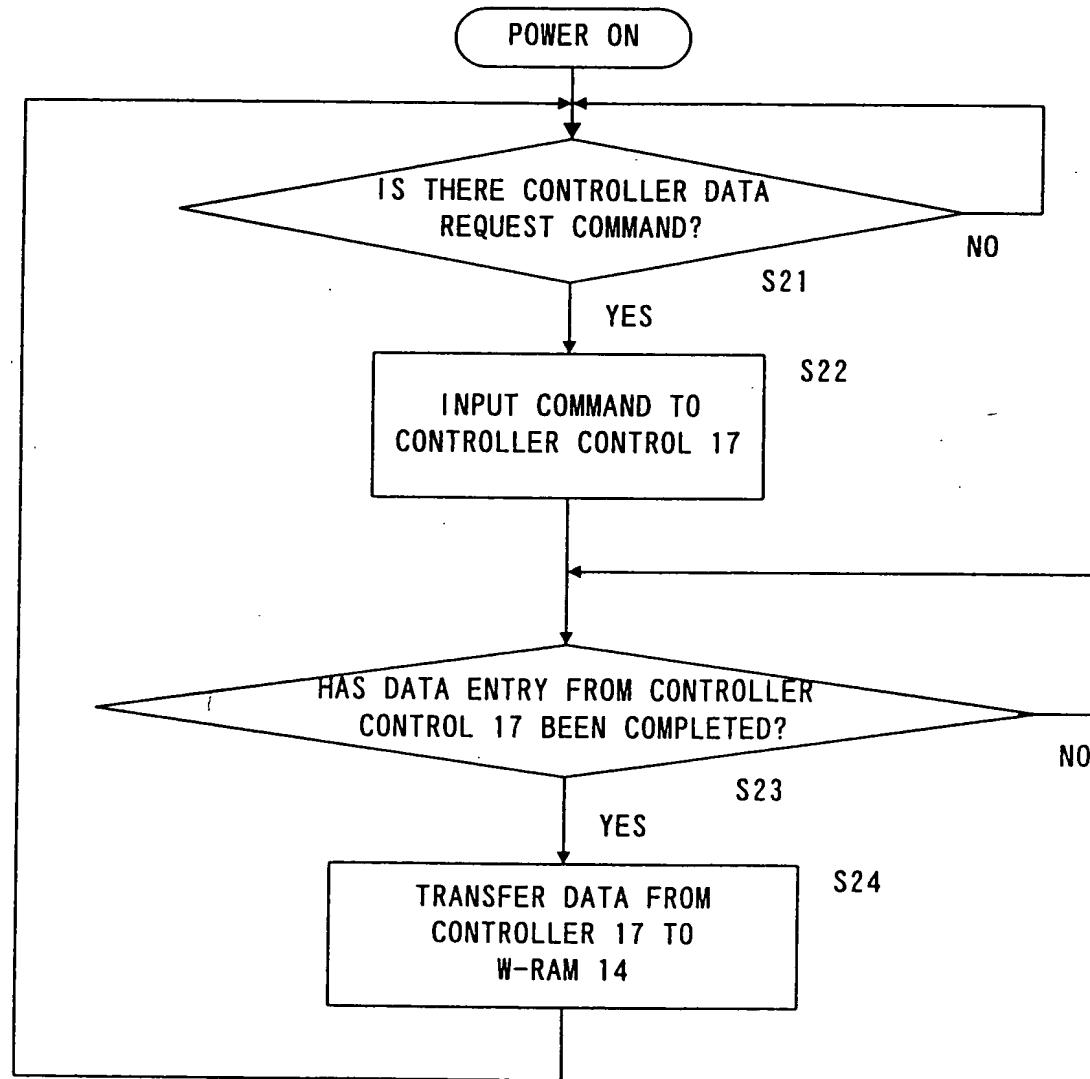
APPROVED	O.G. FIG.
BY	CLASS SUBCLASS
DRAFTSMAN	

FIG. 20



APPROVED BY	O.G. FIG.
DRAFTSMAN	CLASSE SUBCLASS

FIG. 21



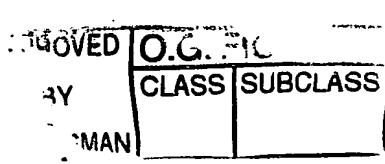
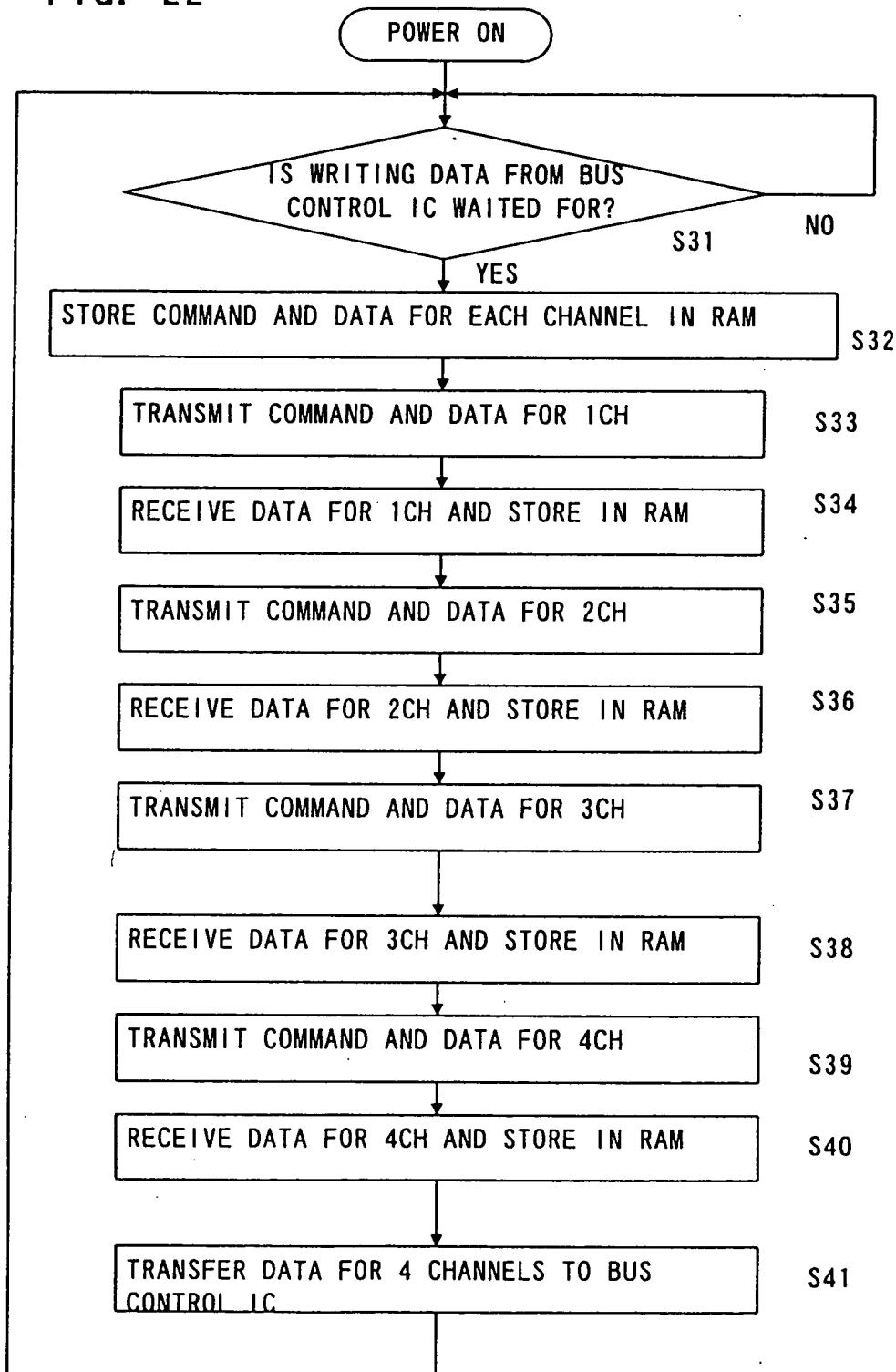
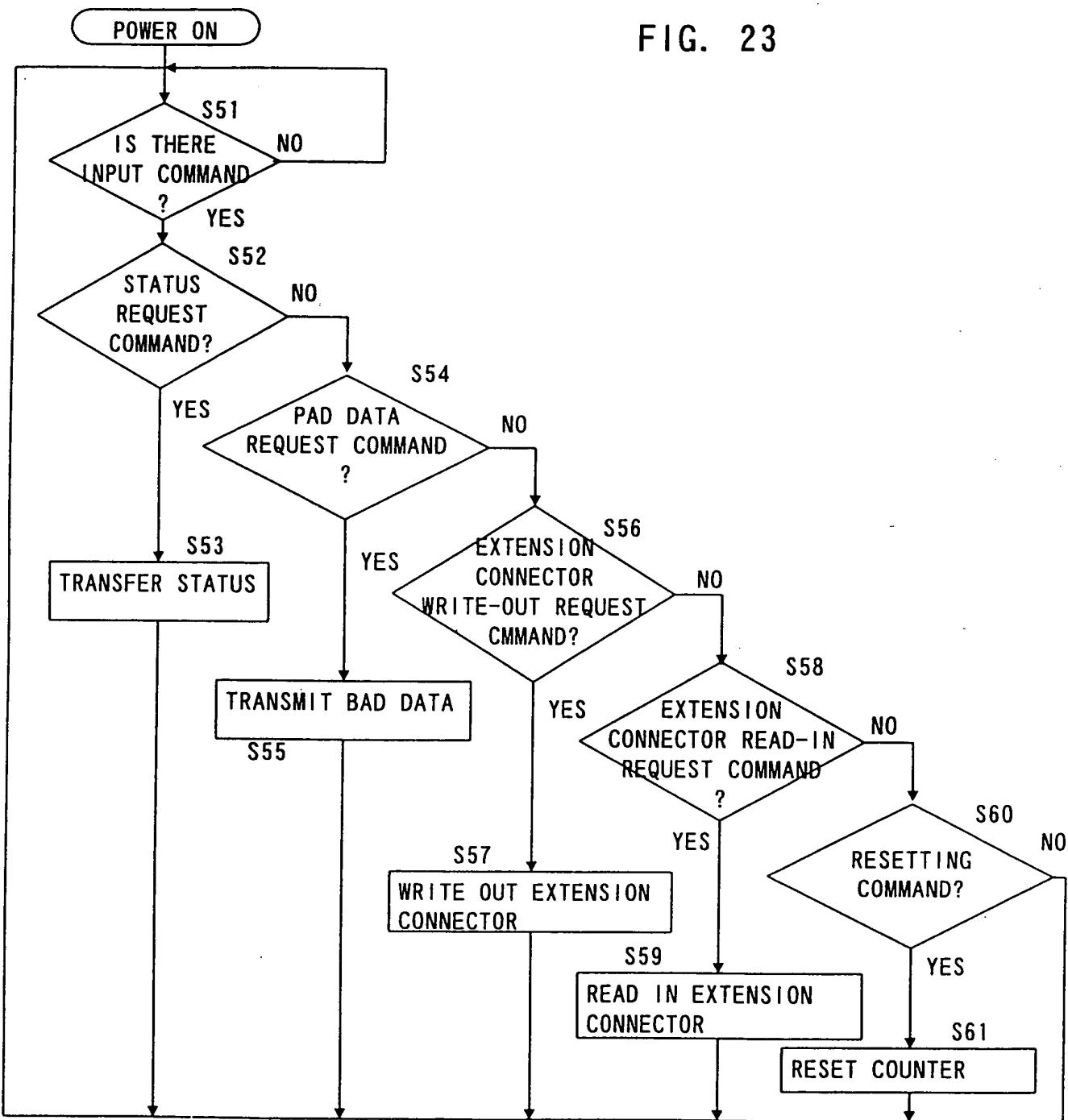


FIG. 22



APPROVED	O.G. FIG.
BY	CLASS SUBCLASS
DRAFTSMAN	

FIG. 23



APPROVED	O.G. FIG
BY	CLASS SUBCLASS
DRAFTSMAN	

FIG. 24

COMMAND 255: CONTROLLER RESETTING
RECEPTION:1 BYTE TRANSMISSION: 3BYTE

	d7	d6	d5	d4	d3	d2	d1	d0
RECEP-TION	1 BYTE	COMMAND 255						
TRANS-MISSION	1 BYTE	TYPE L						
	2 BYTE	TYPE H						
	3 BYTE	STATUS						

APPROVED O.G. FIG.
C 1SF SUBCLASS
CRAFTSMAN

FIG. 25

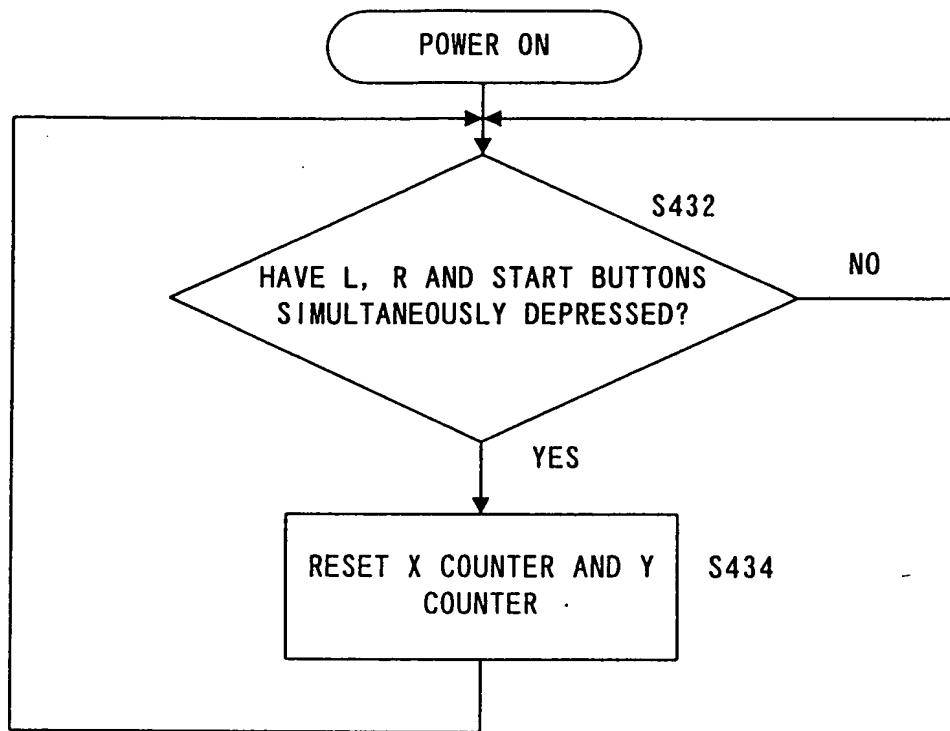


FIG. 26

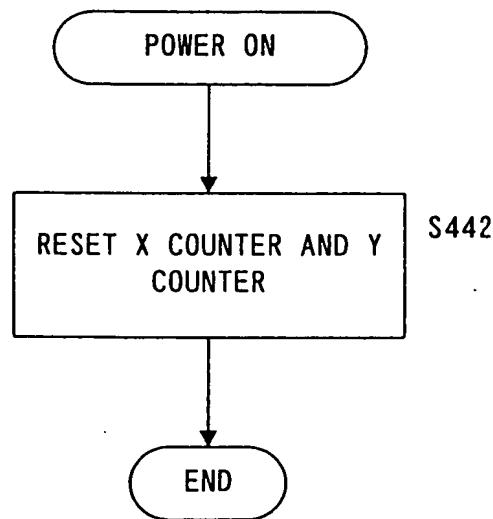
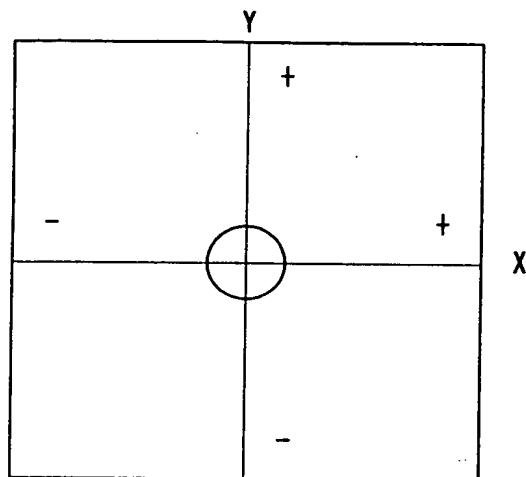


FIG. 27

PHYSICAL COORDINATE OF JOYSTICK



DISPLAY SCREEN

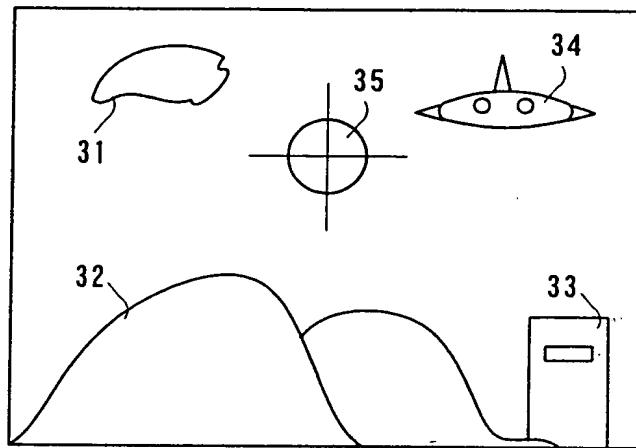
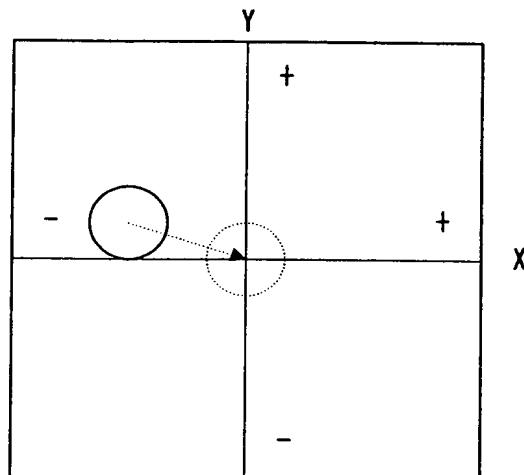
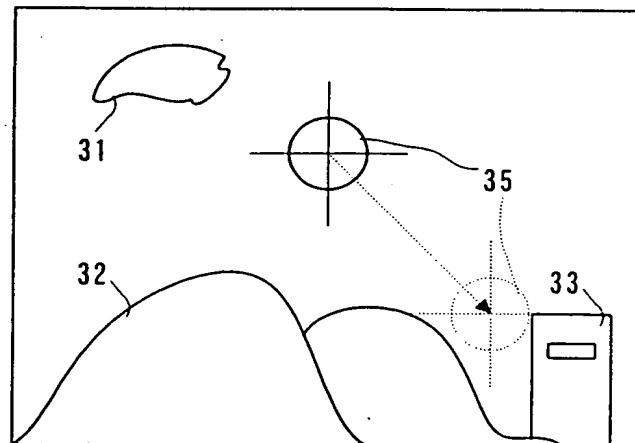


FIG. 28

PHYSICAL COORDINATE OF JOYSTICK



DISPLAY SCREEN



APPROVED	O.G. FIG.	
	CLASS	SUBCLASS
SMAN		

FIG. 29

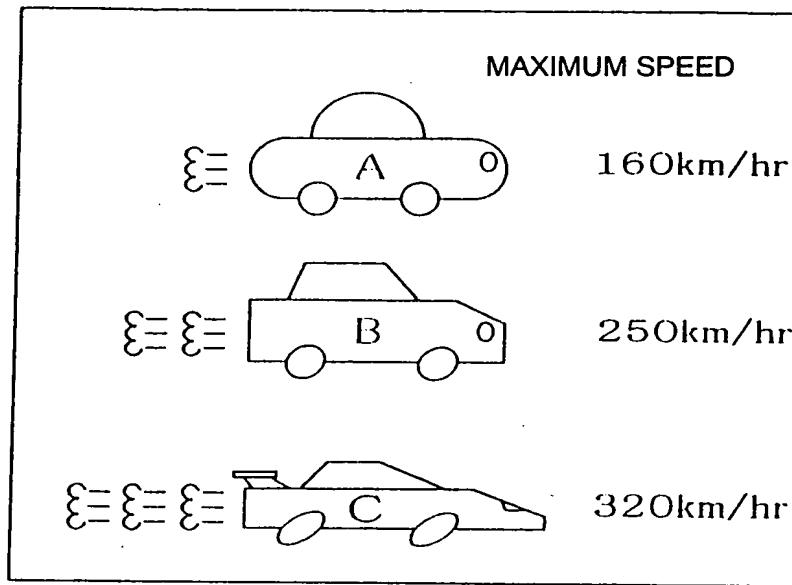


FIG. 30

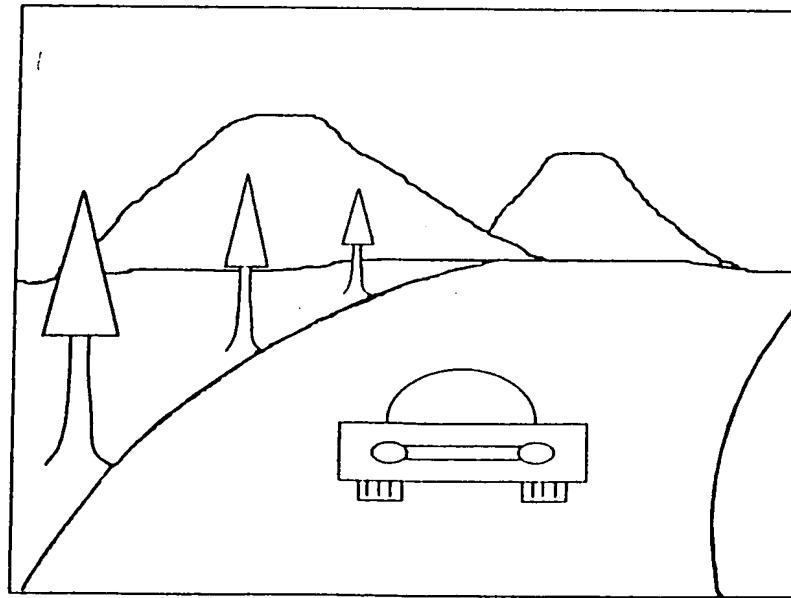
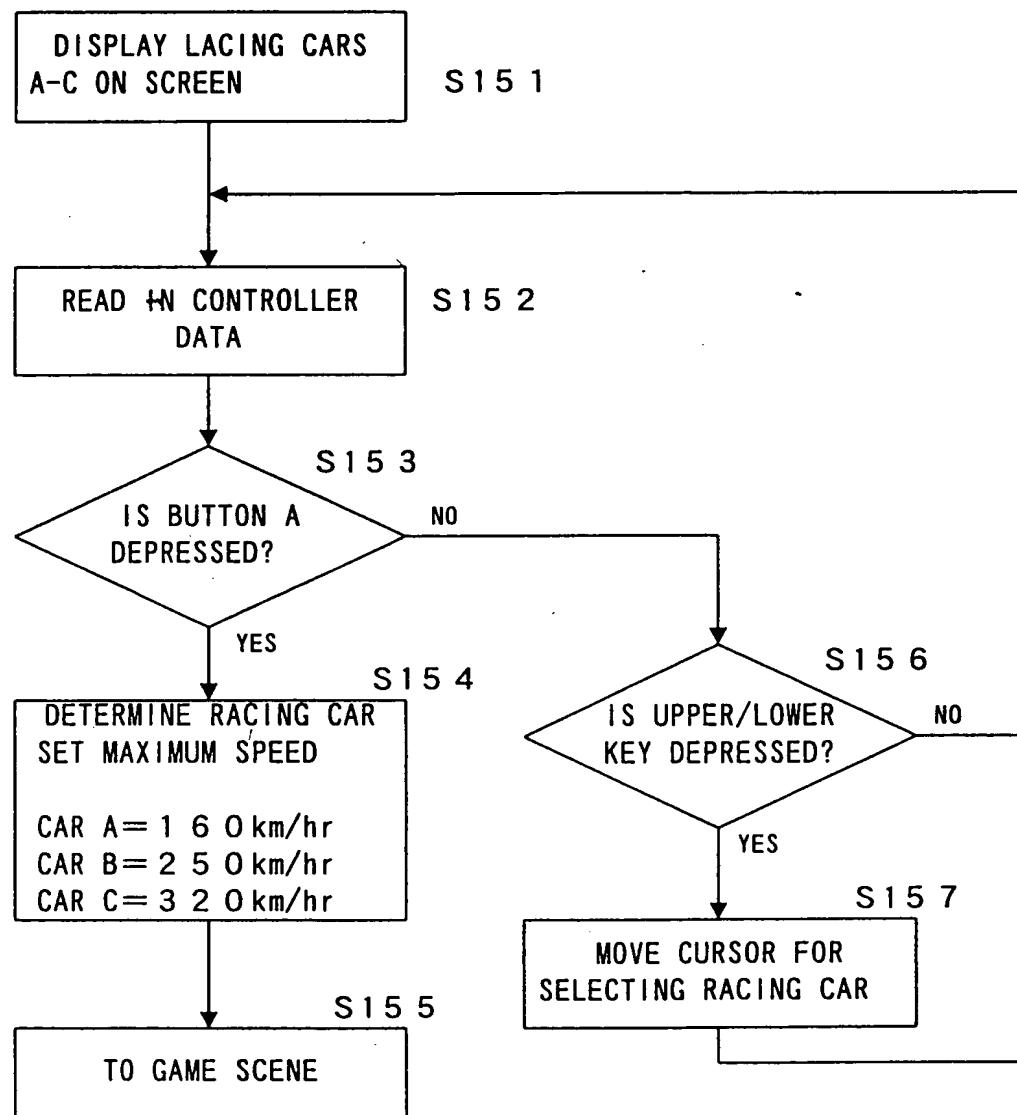
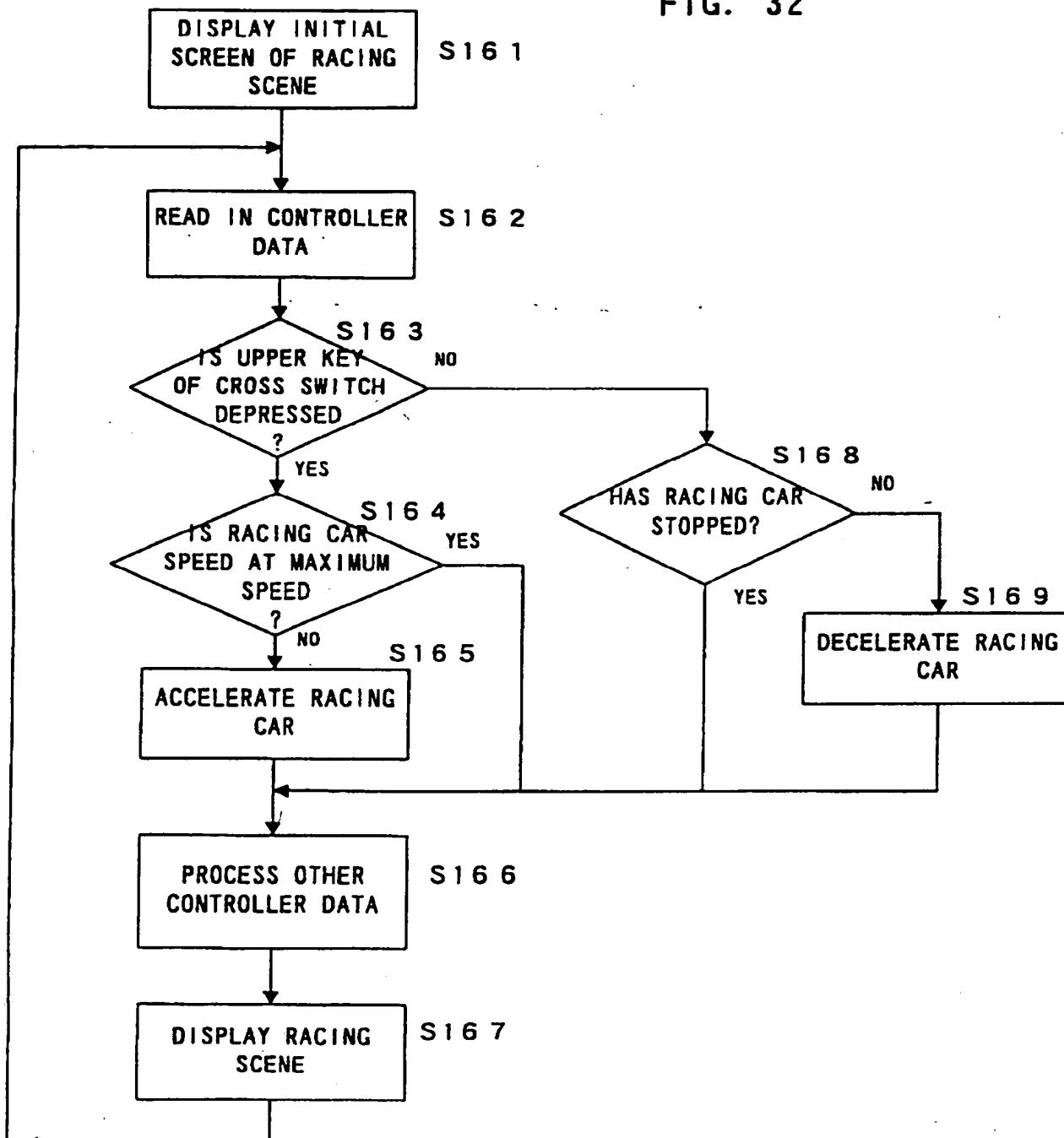


FIG. 31



APPROVED BY O.G. FIG
CLASS SUBCLASS
DRAFTSMAN

FIG. 32



APPROVED	O.G. FIG.
BY	CLASS SUBCLASS
DRAFTSMAN	

FIG. 34

